DMG32240C035_03WTR

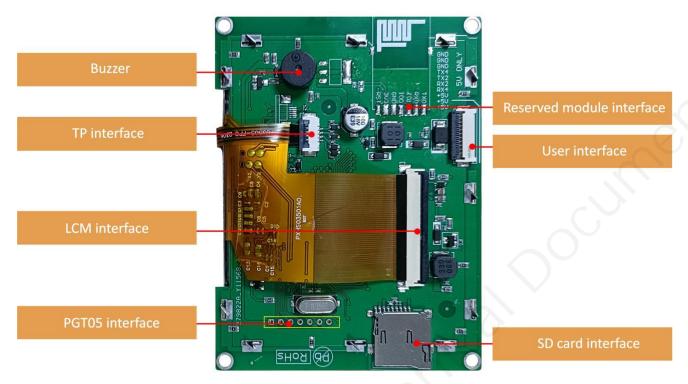
Features:

- Based on T5L1, running DGUS II system, commercial grade.
- 3.5-inch, 320*240 pixels resolution, 16.7M colors, TN-TFT-LCD, normal viewing angle.
- Resistive touch screen.



1. Hardware and interface

1.1Hardware interface



Hardware interface

Disclaimer:

There are multiple parallel raw materials coexisting to guarantee the stable supply. If customers have particular raw material preferences or requirements, please contact the sales personnel.



1.2 Hardware and interface description

No.	Name	Description
1	T5L1 ASIC	Developed by DWIN. Mass production in 2019,1MBytes Nor Flash on the chip, 512KBytes used to store the user database. Rewrite cycle: over 100,000 times
2	LCM interface	FPC54_0.5mm, RGB interface
3	RTP interface	FPC4_1.0mm
4	User interface	10Pin_1.0mm latching socket for power supply and serial communication. Download rate(typical value): 12KByte/s
5	Flash	16MBytes NOR Flash, for fonts, pictures and audio files. Rewrite cycle: over 100,000 times
6	Buzzer	3V passive buzzer. Power: <1W
7	SD card interface	FAT32. Download files by SD interface can be displayed in statistics. Download rate: 4Mb/s
8	Reserved module interface	Wi-Fi module: connect to the cloud platform to update remotely USB module: download files by USB flash disk
9	PGT05 interface	When product crashes by accident, you can use PGT05 to update DGUS kernel and make the product return to normal



2. Specification parameters

2.1Display parameters

LCD Type	TN, TFT LCD
Viewing Angle	Normal viewing angle, 70°/70°/50°/70° (L/R/U/D)
Resolution	320×240 pixels (0°/90°/180°/270°)
Color	24-bit 8R8G8B
Active Area (A.A.)	70.68mm (W)×53.16mm (H)
View Area (V.A.)	70.68mm (W)×53.16mm (H)
Backlight Mode	LED
Backlight Service Life	>20000 hours (Time of the brightness decaying to 50% on the condition of continuous working with the maximum brightness)
Brightness	200nit
Brightness Control	0~100 grade (When the brightness is adjusted to 1%~30% of the maximum brightness, flickering may occur and is not recommended to use in this range)
Note:You can use dynan	nic screen saver wallpapers to avoid afterimages caused by fixed page display

Note: You can use dynamic screen saver wallpapers to avoid afterimages caused by fixed page display for a long time.

2.2 Touch parameters

Туре	RTP (Resistive touch panel)
Structure	ITO film + ITO glass
Touch Mode	Single point touch, support continuous sliding touch
Surface Hardness	3H
Light Transmittance	Over 80%
Life	Over 1,000,000 times touch



2.3 Serial interface parameters

Mode	UART2: TTL/CMOS UART4: TTL/CMOS (Only available after OS configuration)						
	Test Condition	Min	Тур	Max	Unit		
	Output 1, lout = 1mA	3.0	3.3	-	V		
Voltage Level	Output 0, lout = -1mA	-	0	0.3	V		
	Input 1, lin = 1mA	2.4	3.3	5.0	V		
	Input 0, lin = -1mA	0	-	0.5	V		
Baud Rate	3150~3225600bps, typical value of 115200bps						
Data Format	UART2: N81 UART4: N81/E81/O81/N82 , 4 modes (OS configuration)						
Interface Cable	10Pin_1.0mm						

2.4 Electrical specifications

Rated Power	<5W	<5W			
Operating Voltage 4.5~5.5V, typical value of 5V					
	170mA	VCC=5V, max backlight			
Operating Current	80mA	VCC=5V, backlight off			
Recommended power supply: 5V 1A DC					

2.5 Operating environment

Operating Temperature	-20℃~70℃ (5V @ 60% RH)
Storage Temperature	-30℃~80℃
Conformal Coating	None
Operating Humidity	10%~90%RH, typical value of 60% RH



3. Reliability test

3.1 Electrostatic discharge test

Test process: the product was placed on the test bench to perform contact and air discharge in turn of the serial screen iron frame and display area. During the experimental process, it was observed whether the screen is dead, black, white, splash, or reboot. According to the experiment results, the performance is in line with the criteria GB/T 17626.2 B level and above.

■ Test standard : □EN 61000-4-2:2009	☑IEC 61000-4-2:2008	□GB/T 17626.2-2018
□Other:		

Table 1: Electrostatic Discharge Immunity (Air Discharge)

Test Points Locations	Test Levels							
	-2kV	+2kV	-4kV	+4kV	-8kV	+8kV	-15kV	+15kV
Screen			A	A	A	A		
1	/	/	1	1	1	1	1	1
/	1	1	. /	1	1	1	1	1

Table 2: Electrostatic Discharge Immunity (Direct Contact)

Test Points Locations	Test Levels							
	-2kV	+2kV	-4kV	+4kV	-6kV	+6kV	-8kV	+8kV
Frame			A	A	A	A		
/	/	/	1/	01	1	1	1	1
/	/	/	/	/	/	1	1	1

Performance Criterion:

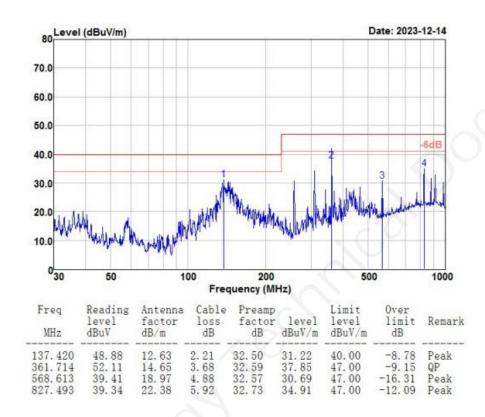
- A. Normal performance within limits specified by the manufacturer, requestor or purchaser;
- B. Temporary loss of function or degradation of performance which ceases after the disturbance ceases, and from which the equipment under test recovers its normal performance, without operator intervention;
- C. Temporary loss of function or degradation of performance, the correction of which requires operator intervention;
- D. Loss of function or degradation of performance which is not recoverable, due to damage to hardware or software. or loss of data.

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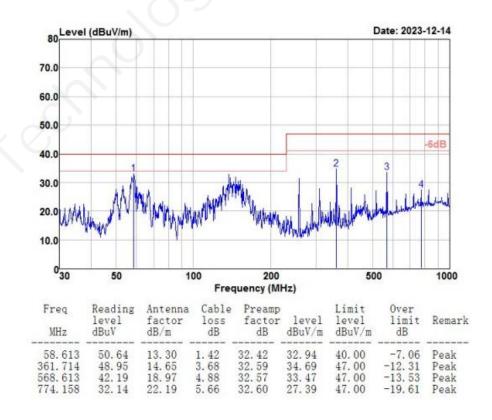
3.2 RE test

Test Item Test Standard		Result		
RE	Class B	Normal operation		

HORIZONTAL



VERTICAL



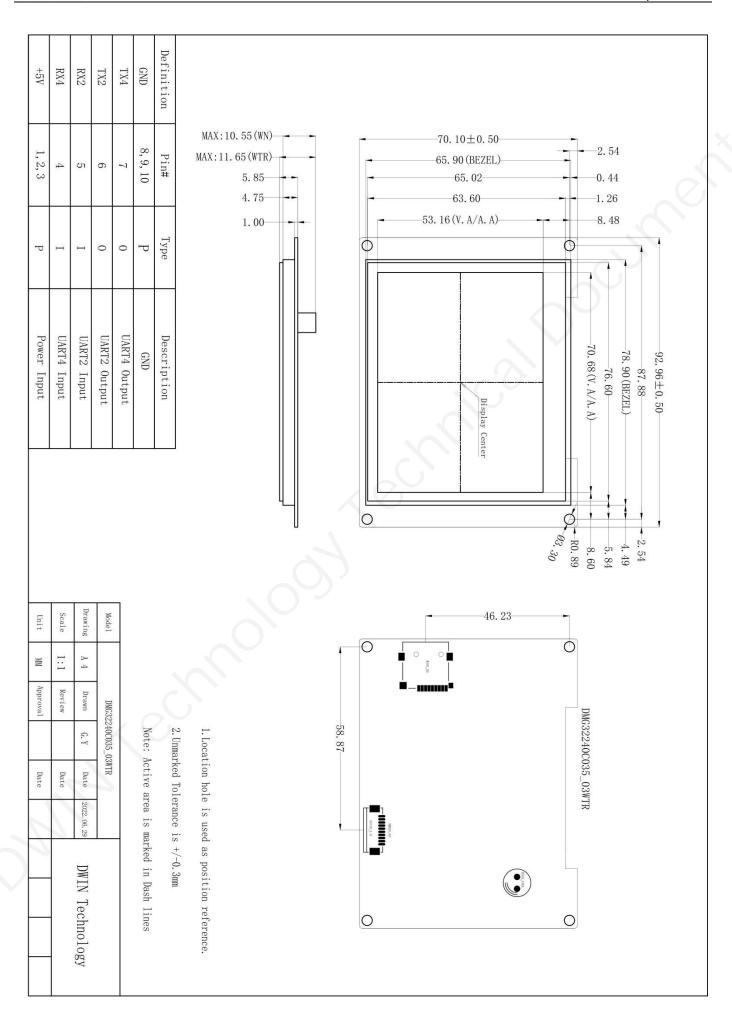


4. Packaging & dimensions

Form Factor	92.96mm (W)×70.10mm (H)×11.65mm (T)
Installation Dimensions	Positioning hole: 78.90(+0.3mm)×65.90(+0.3mm)
Net Weight	65g

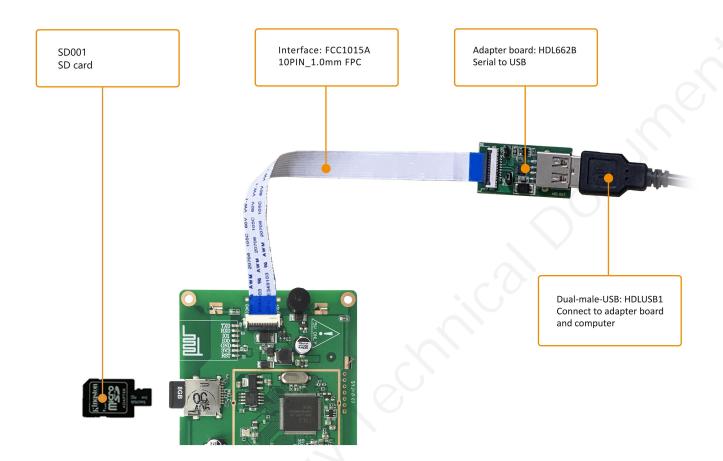
Packaging Standards

Model	Dimensions	Layer	Quantity/Layer	Quantity(Pcs)
Carton1:	220mm(L)×160mm(W)×47mm (H)	1	2	2
Carton2:	250mm(L)×200mm(W)×80mm (H)	1	4	4
Carton3:	320mm(L)×270mm(W)×80mm (H)	1	8	8
Carton4:	450mm(L)×350mm(W)×300mm(H)	2	30	60
Carton5:	600mm(L)×450mm(W)×300mm(H)	3	50	150



5. Debugging tools

It is recommended for new users of DWIN smart LCMs to purchase official accessories. For more details, please refer to customer service center.



6. T5L series IC features

- (1) Mature and stable 8051 core which is the most widely used with the maximum operating frequency of T5L is up to 250MHz, 1T(single instruction cycle)high speed operation.
- (2) Separate GUI CPU Core running DGUS II System:
 - High-speed display memory, 2.4GB/S bandwidth.
 - 2D hardware acceleration, the decompression speed of JPEG is up to 200fps@1280*800 and the UI with animation and icons as its main feature is extremely cool and smooth.
 - Images and icons stored in JPEG format. Adopt Low-cost 16Mbytes SPI Flash.
 - Support CTP or RTP with adjustable sensitivity and maximum 400 Hz touch frequency.
 - 1-way 15bit 32Ksps PWM digital power amplifier driver loudspeaker, save power amplifier cost and achieve high signal-to-noise ratio and sound quality restoration.
 - 128Kbytes variable storage space for exchanging data with OS CPU Core and memory.
 - Support DGUS development and simulation on PC. Support background remote upgrade.
- (3) Separate CPU (OS CPU) core runs user 8051 code or DWIN OS system and user CPU is omitted in practical application:
 - Standard 8051 architecture and instruction set, 64Kbytes code space, 32Kbytes on-chip RAM.
 - 64 bit integer mathematical operation unit (MDU), including 64 bit MAC and 64 bit divider.
 - 28 IOs, 4-channel UARTs, 1-channel CAN, up to 8-channel 12-bit A/Ds and 2-channel 16-bit PWM of adjustable resolution.
 - Support IAP on-line simulation and debugging with unlimited number of breakpoints.
 - Upgrade code online through DGUS system.
- (4) 1Mbytes on-chip Flash with DWIN patent encryption technology ensure code and data security.
- (5) Operating temperature ranges from -40°C to +85°C(IC operating temperature customizable from -55°C to 105°C).

DWIN encourages users to design your own customized product based on T5L



7. Revision records

Rev	Revise Date	Content	Editor
00	2022-12-13	First Edition	Xu Ying
01	2024-01-05	EMC hardware upgrade, natural consumption of old inventory boards	Xu Ying

Please contact us if you have any questions about the use of this document or our products, or if you would like to know the latest information about our products:

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Thank you all for continuous support of DWIN, and your approval is the driving force of our progress!

Important Disclaimer

DWIN reserves the right to make any changes to product designs without prior notice.

Customers should ensure strictly adhering to all the relevant standards and requirements during the product application process, including but not limited to functional safety, information security, and regulatory provisions.

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