1W, Fixed input voltage, isolated & unregulated dual output









FEATURES

- Operating temperature range: -40°C to +105°C
- High efficiency up to 82%
- Compact SMD package
- Isolation voltage: 3K VDC
- Internal surface mounted design
- No external component required
- International standard pin-out
- IEC60950, UL60950, EN60950 approval

E_XT-1WAR2 series are specially designed for applications where an isolated voltage is required in a distributed power supply system. They are suitable for

- 1. Where the voltage of the input power supply is stable (voltage variation: $\pm 10\% Vin$);
- Where isolation between input and output is necessary (isolation voltage ≤3000VDC);
- 3. Where the output voltage regulation is not strictly required;
- 4. Typical application: preceding-stage interference isolation condition; ground-interference canceled condition; digit circuit condition; Voltage-isolation converting condition; normal low-frequency artificial circuit condition; relay drive circuit condition, etc.

		Input Voltage (VDC)	C	Output	Efficiency (9/ Min /Time)	Max. Capacitive	
Certification	Part No.	Part No. Nominal Output Voltage Output Current (m. (Range) (VDC) (Max./Min.)		Output Current (mA) (Max./Min.)	Efficiency (%,Min./Typ.) @ Full Load	Load (µF)	
E0305XT-1WAR		3.3	±5	±100/±10	72/76		
CE	E0312XT-1WAR2	(2.97-3.63)	±12	±42/±5	73/77		
	E0505XT-1WAR2		±5	±100/±10	76/80		
	E0509XT-1WAR2	_	±9	±56/±6	76/80		
	E0512XT-1WAR2	5 (4.5-5.5)	±12	±42/±5	75/79		
	E0515XT-1WAR2	(4.0 0.0)	±15	±33/±3	77/81		
LII /CE/CD	E0524XT-1WAR2		±24	±21/±2	77/81		
UL/CE/CB	E1205XT-1WAR2	12 (10.8-13.2)	±5	±100/±10	76/80		
	E1209XT-1WAR2		±9	±56/±6	76/80		
	E1212XT-1WAR2		±12	±42/±5	77/81	100	
	E1215XT-1WAR2		±15	±33/±3	77/81		
	E1224XT-1WAR2		±24	±21/±2	77/81		
CE	E1515XT-1WAR2	15 (13.5-16.5)	±15	±33/±3	77/81		
	E2405XT-1WAR2		±5	±100/±10	76/80		
	E2409XT-1WAR2		±9	±56/±6	76/80		
UL/CE/CB	E2412XT-1WAR2	24 (21.6-26.4)	±12	±42/±5	77/81		
	E2415XT-1WAR2	(21.0 20.7)	±15	±33/±3	78/82		
	E2424XT-1WAR2	-	±24	±21/±2	72/76		

Note: *The capacitive loads of positive and negative outputs are identical.

Input Specification	ns				
Item	Operating Conditions	Min.	Тур.	Max.	Unit
	3.3V input		389/25	/70	
	5V input		250/20	/60	
Input Current (full load / no-load)	12V input		104/15	/50	mA
(.a 10 a.a. / 1.0 10 a.a.)	15V input		83/12	-35	
	24V input		52/10	/30	

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Reflected Ripple Current			15		mA
	3.3V input	-0.7		5	
	5V input	-0.7		9	
Surge Voltage (1sec. max.)	12V input	-0.7		18	VDC
	15V input	-0.7		21	
	24V input	-0.7		30	
Input Filter			Filter co	apacitor	
Hot Plug			Unavo	ailable	

Item	Operating Condition	ns	Min.	Тур.	Max.	Unit
Output Voltage Accuracy			See tolerance envelope curve(Fig. 1)			
Line Regulation	Input voltage chan	ge: ±1%		-	±1.2	
	10%-100% load	5VDC output		12		%
		9VDC output		9		
Load Regulation		12VDC output		8		
		15VDC output		7		
		24VDC output		6		
Ripple & Noise*	20MHz bandwidth	20MHz bandwidth		60	150	mVp-p
Temperature Coefficient	Full load			-	±0.03	%/℃
Ob and Observit Deads at a set	E03xxXT-1WAR2/E24xxXT-1WAR2/E0524XT-1WAR2 Others		-	-	1	s
Short Circuit Protection**			Continuous, self-recovery			

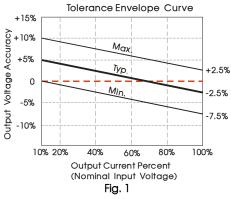
^{**}Supply voltage must be discontinued at the end of short circuit duration for E03xxXT-1WAR2 series, E0524XT-1WAR2 model and E24xxXT-1WAR2 series.

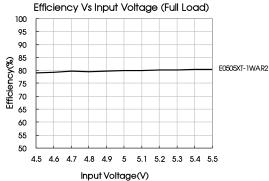
General Specification	ns					
Item	Operating Conditions	Min.	Тур.	M	lax.	Unit
Isolation Voltage	Input-output, with the test time of 1 minute and the leak current lower than 1mA	3000				VDC
Isolation Resistance	Input-output, isolation voltage 500VDC	1000			-	$M\Omega$
Isolation Capacitance	Input-output, 100KHz/0.1V		20		-	рF
Operating Temperature	Derating when operating temperature up to 100°C , (see Fig. 2)	-40		1	05	
Storage Temperature		-55		1	25	°C
Casing Temperature Rise	Ta=25°C, nominal input, full load output		25		-	C
Pin Welding Resistance Temperature	Welding spot is 1.5mm away from the casing, 10 seconds			300		
Storage Humidity	Non-condensing				95	%RH
Reflow Soldering Temperature		Peak temp. at 217°C. For actual a J-STD-020D.	pplication,			
Switching Frequency	Full load, nominal input voltage			100	-	KHz
MTBF	MIL-HDBK-217F@25℃ 3500 -				K hours	

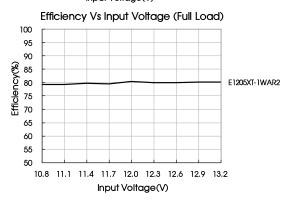
Physical Specifications		
Casing Material	Black flame-retardant and heat-resistant Epoxy resin (UL94 V-0)	
Dimensions	15.24*11.20*7.25 mm	
Weight	2.0g (Typ.)	
Cooling Method	Free air convection	

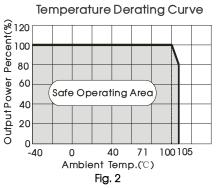
EMC Specifications				
EMI	CE	CISPR32/EN55032 CLASS B (see Fig. 4 for recommended circuit)		
EIVII	RE	CISPR32/EN55032 CLASS B (see Fig. 4 for recommended circuit)		
EMS	ESD	IEC/EN61000-4-2 Contact ±6KV perf. Criteria B		

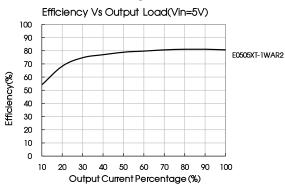
Product Characteristic Curve

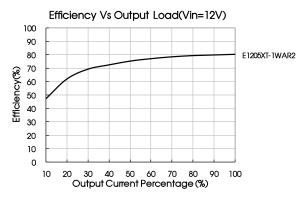








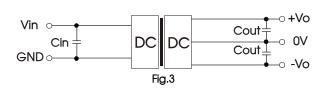




Design Reference

1. Typical application circuit

If it is required to further reduce input and output ripple, a filter capacitor may be connected to the input and output terminals, see Fig.3. Moreover, choosing a suitable filter capacitor is very important, start-up problems may be caused if the capacitance is too large. Under the condition of safe and reliable operation, the recommended capacitive load values are shown in Table 1.



	Recommended capacitive load value table (Table 1)					
Vin(VDC) Cin(µF		Cin(µF)	Vo (VDC)	Cout(µF)		
3.3 4.7		±5	4.7			
5 4.7		±9	2.2			
	12	2.2	±12	1		
15 2.2		±15	1			
	24	1	±24	0.47		

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EMC solution-recommended circuit

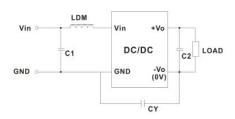


Fig. 4

Input voltage (VDC) 3.3/5/12 15/24 4.7µF /50V C1 C2 Refer to the Cout in Fig.3 **EMI** LDM 6.8µH CY 1nF/3KV

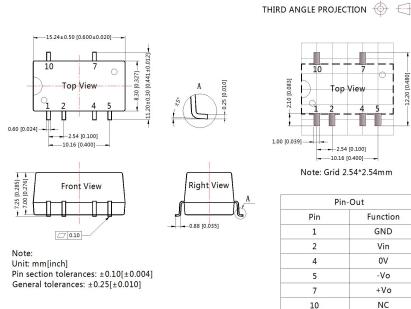
Note: 1. 15/24V input series, 24V output series is subject to CY (CY: 1nF/3KV). 2. It is not needed to add the component in the peripheral circuit when parameter with the symbol of "--".

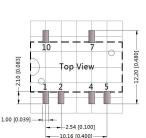
3. Output load requirements

In order to ensure the converter can work reliably with high efficiency, the minimum load should not less than 10% rated load when it is used. If the needed power is indeed small, please parallel a resistor on the output side (The sum of the efficient power and resistor consumption power is not less than 10%).

4. For more information please find DC-DC converter application notes on www.mornsun-power.com

Dimensions and Recommended Layout





Note: Grid 2.54*2.54mm

Pin-Out		
Pin	Function	
1	GND	
2	Vin	
4	0V	
5	-Vo	
7	+Vo	
10	NC	

NC: Pin to be isolated from circuitry

Notes:

- Packing information please refer to Product Packing Information which can be downloaded from www.mornsun-power.com. Packing bag number: 58210023;
- If the product is not operated within the required load range, the product performance cannot be guaranteed to comply with all parameters in the datasheet:
- The maximum capacitive load offered were tested at input voltage range and full load;
- Unless otherwise specified, parameters in this datasheet were measured under the conditions of Ta=25°C, humidity<75%RH with nominal input voltage and rated output load;
- All index testing methods in this datasheet are based on our Company's corporate standards;
- We can provide product customization service, please contact our technicians directly for specific information;
- Products are related to laws and regulations: see "Features" and "EMC";
- Our products shall be classified according to ISO14001 and related environmental laws and regulations, and shall be handled by qualified units.

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