

Wide input voltage non-isolated and regulated single output





FEATURES

- Ultra-small, ultra-thin DFN package(9.00 x 7.00 x 3.10mm)
- Operating ambient temperature range: -40 $^{\circ}{\rm C}$ to +105 $^{\circ}{\rm C}$
- High efficiency up to 94%
- No-load input current as low as 0.1mA
- Continuous short circuit protection
- EN62368 approved
- Meets AEC-Q100 (under testing)

K78_MT-1000R4 series are high efficiency switching regulators. The converters feature high efficiency, low loss and short-circuit protection in a compact DFN package. These products are widely used in applications such as industrial control, instrumentation and electric power.

		Input Voltage (VDC)*	C	Output	Full Load	Capacitive	
Certification	Part No.	Nominal (Range)	Voltage (VDC)	Current (mA) Max.	Efficiency (%) Typ. Vin Min./ Vin Nominal / Vin Max.	Load (µF)	
	K7803MT-1000R4	24 (4.75-36)	3.3	1000	89/84/81	680	
	K/003IVII-1000R4	12 (8-27)	-3.3	-500	85/85/81	330	
	K7805MT-1000R4	24 (6.5-36)	5	1000	92/87/84	680	
	K/803WI-1000R4	12 (8-27)	-5	-500	85/85/83	330	
	K78X6MT-1000R4	24 (8-36)	6.5	1000	92/88/86	680	
CE		12 (8-24)	-6.5	-500	83/85/84	330	
	1/7000N AT 1000D A	24 (12-36)	9	1000	92/90/87	680	
	K7809MT-1000R4	12 (8-24)	-9	-500	81/85/84	330	
	1/7010N#T 1000D#	24 (15-36)	12	1000	94/91/89	680	
_	K7812MT-1000R4	12 (8-20)	-12	-300	83/85/84	330	
	1/7015N#T 1000D#	24 (18-36)	15	1000	94/93/90	680	
	K7815MT-1000R4	12 (8-18)	-15	-300	82/84/84	330	

noie: For input voltage exceeding 50 VDC, an input capacitor of 22ur/50v is required.

Input Specifications						
Item	Operating Conditions	Min.	Тур.	Max.	Unit	
No-load Input Current	Nominal input voltage		0.1		mA	
Reverse Polarity at Input			Avoid / Not protected			
Input Filter			Capacitance filter			
01.18	Module on	Ctrl pin open or pulled high(TL 1.6~5VDC)				
Ctrl*	Module off	Ctrl pin	Ctrl pin pulled low to GND(-Vo)(0~0.6VDC)			

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DC/DC Converter K78_MT-1000R4 Series



Nominal input voltage, input current when off		240		uA				
Note: *The positive output ctrl pin voltage is referenced to input GND; Negative output ctrl pin voltage is referenced to -Vo.								

Output Specification	าร					
Item	Operating Conditions		Min.	Тур.	Max.	Unit
\/-\ -\	Full load, input	3.3 VDC output		±2	±4	
Voltage Accuracy	voltage range	Others	-	±2	±3	0/
Linear Regulation	Full load, input voltage re	ange	-	±0.2		%
Load Regulation	Nominal input voltage, 1	0% -100% load		±1.0		
	20MHz bandwidth, nomi	-	75	150		
Ripple & Noise*	20MHz bandwidth, nomi external capacitor 22µF		20	75	mVp-p	
Temperature Coefficient	Operating temperature	-40°C to +105°C		±0.02		%/ °C
Town double Double on Double or	Nominal input voltage,	3.3 V/5V/6.5V/9VDC output		50	150	>/
Transient Response Deviation	25% load step change	12V/15VDC output	-	100	300	mV
Transient Recovery Time	Nominal input voltage, 2	5% load step change		0.1	0.8	ms
Short-circuit Protection					self-recovery	,
Trim	Input voltage range		±10		%Vo	
Note: * The "parallel cable" metho	d is used for ripple and noise t	est, please refer to DC-DC Converter A	Application Not	tes for specific	information;	

General Specificatio	ns				
Item	Operating Conditions	Min.	Тур.	Max.	Unit
Operating Temperature	See Fig. 1	-40		+105	°C
Storage Temperature		-55		+125	
Storage Humidity	Non-condensing	5		95	%RH
Reflow Soldering Temperature			217℃. Also re	5°, duratior efer to IPC/JE	
Switching Frequency	Full load, nominal input voltage		1.0		MHz
MTBF	MIL-HDBK-217F@25°C	8552			k hours
Moisture Sensitivity Level (MSL)	IPC/JEDEC J-STD-020D.1	Level 3			
Pollution Degree		PD3			

Mechanical Specifications					
Case Material	Black epoxy resin; flame-retardant and heat-resistant(UL94 V-0)				
Dimensions	9.00 ×7.00 × 3.10 mm				
Weight	0.58g(Typ.)				
Cooling Method	Free air convection				

Electron	Electromagnetic Compatibility (EMC)							
Emissions	CE	CISPR32/EN55032	CLASS B (see Fig. 3-2) for recommended circuit)					
RE		CISPR32/EN55032	CLASS B (see Fig. 3-2) for recommended circuit)					
	ESD*	IEC/EN 61000-4-2	Contact ±6kV	perf. Criteria B				
	RS	IEC/EN 61000-4-3	10V/m	perf. Criteria A				
Immunity	CS	IEC/EN 61000-4-6	3Vr.m.s	perf. Criteria A				
	EFT	IEC/EN 61000-4-4	$\pm1\text{kV}$ (see Fig. 3-1) for recommended circuit)	perf. Criteria B				
	Surge	IEC/EN 61000-4-5	line to line $\pm1\text{kV}$ (see Fig. 3-1) for recommended circuit)	perf. Criteria B				
Note: * The sto	atic level of the Ctrl 8	Trim pin is ±2kV when th	ney are not connected to external devices; It is suggested to connect an ex	ternal capacitor				

(225K/50V) from Ctrl to GND/-Vo to meet ESD (±6kV) of the Ctrl pin, and to connect a varistor (22V/30A) from Trim to GND/-Vo to meet ESD(±6kV) of the Trim pin.

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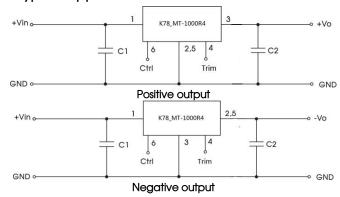
Typical Characteristic Curves

9/12/15V output Temperature Derating Curve 120 100 100 80 70 60 40 26V < Vin < 36V Safe Operating Area Operating Temperature (°C)

Fig. 1

Design Reference

1. Typical application



	CI	C2	Ra I/Ra2				
Part No.	(ceramic	(ceramic	(Trim				
	capacitor)	capacitor)	resistance)				
K7803MT-1000R4		22µF/10V					
K7805MT-1000R4		22µF/10V					
K78X6MT-1000R4	10 [(50) (22µF/16V	Refer to Trim				
K7809MT-1000R4	10µF/50V	22µF/16V	resistance calculation				
K7812MT-1000R4		22µF/25V	Galcalanon				
K7815MT-1000R4		22µF/25V					
Table 1							

Fig. 2 Typical application circuit

Notes:

- 1. The required C1 and C2 capacitors must be connected as close as possible to the terminals of the module;
- 2. Refer to Table 1 for C1 and C2 capacitor values. For certain applications, increased values and/or tantalum or low ESR electrolytic capacitors may also be used instead;
- 3. Converter cannot be used for hot swap and with output in parallel.

2. EMC compliance circuit

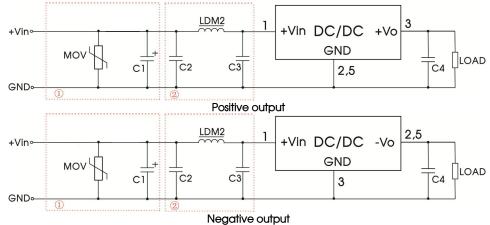


Fig.3 Recommended compliance circuit

		-				
Part No.	MOV	C1	C2	LDM2	СЗ	C4
K7803/05/X6MT-1000R4 (Positive output)	S20K30	680µF /50V	10µF/50V	68µH		22µF/25V
Others	S20K30	680µF /50V	10µF/50V	68µH	10µF/50V	22µF/25V

Notes: For EMC tests we use Part ① in Fig.3 for immunity and part ② for emissions test. Selecting based on needs.



3. Trim Function for Output Voltage Adjustment (open if unused)

1. Positive output application: connect trim resistor to GND/Vo respectively for adjusting up/down.

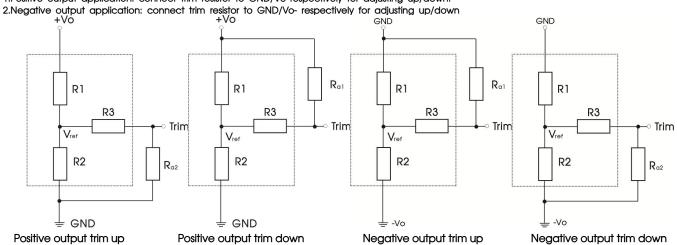


Fig.4 Circuit diagram of Trim up and down (dashed line shows internal part of module)

Calculating Trim resistor values:

Trim up:
$$R_{a2} = \frac{aR_2}{R_2 - a} - R_3$$
, $a = R_2 / / (R_3 + R_{a2}) = \frac{V_{\text{ref}}}{V_o - V_{\text{ref}}} R_1$

Trim down:
$$R_{a1} = \frac{aR_1}{R_1 - a} - R_3$$
, $a = R_1 / (R_3 + R_{a1}) = \frac{V_o - V_{ref}}{V_{ref}} R_2$

		1	101	
Vout(V)	R1(K Ω)	R2(K Ω)	R3(K Ω)	Vref(V)
3.3	150	33	180	0.6
5	100	13.66	82	0.6
6.5	32.4	3.3	20	0.6
9	100	7.14	47	0.6
12	100	5.28	43	0.6
15	180	7.5	51	0.6

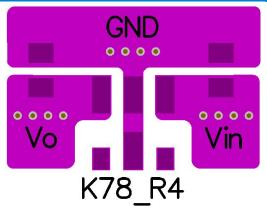
Table:

Vout nom.	±3.3	VDC	±5.0	VDC	±6.5	VDC	±9.0\	VDC	±12\	/DC	±15\	/DC
Vout Trim.	Ra1 (KΩ)	Ra2 (KΩ)	Ra1 (KΩ)	Ra2 (KΩ)	Ral (KΩ)	Ra2 (KΩ)	Ra1 (KΩ)	Ra2 (KΩ)	Ra1 (KΩ)	Ra2 (KΩ)	Ra1 (KΩ)	Ra2 (KΩ)
2.97	815	-	-	-	-	-	-	-	-	-	-	-
3.63	-	117.3	-	-	-	-	-	-	-	-	-	-
4.5	-	-	710	-	-	-	-	-	-	-	-	-
5.5	-	-	-	36.2	-	-	-	-	-	-	-	-
5.85	-	-	-		245.4	-	-	-	-	-	-	-
7.15	-	-	-	-	-	9.5	-	-	-	-	-	-
8.1	-	-	-	-	-	-	783.2	-	-	-	-	-
9.9	-	-	-	-	-	-	-	19.9	-	-	-	-
10.8	-	-	-	-	-	-	-	-	833.5	-	-	-
13.2	-	-	-	-	-	-	-	-	-	5.5	-	-
13.5	-	-	-	-	-	-	-	-	-	-	1497	-
16.5	-	-	-	-	_	-	-	-	-	-	-	21

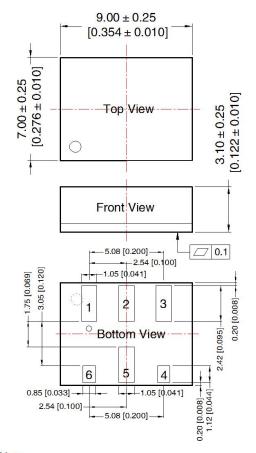
4. For additional information please refer to DC-DC converter application notes on www.mornsun-power.com



Temperature Rise Test PCB Layout



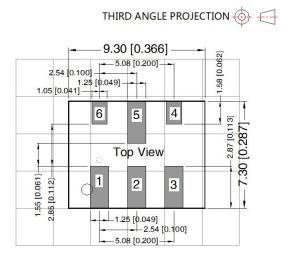
Dimensions and Recommended Layout



Note:

Unit: mm[inch]

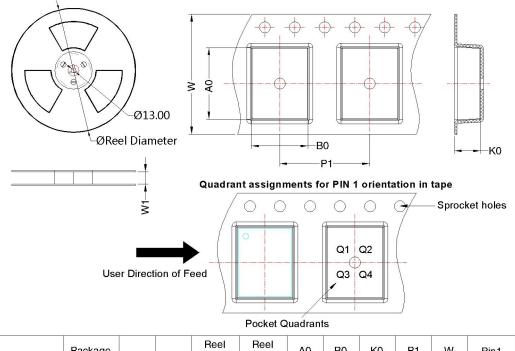
Pin diameter tolerances: $\pm 0.10[\pm 0.004]$



Note: Grid 2.54*2.54mm

	Pin-Out							
Pin	Positive output	Negative output						
1.	+Vin	+Vin						
2	GND	-Vo						
3	+Vo	GND						
4	Trim	Trim						
5	GND	-Vo						
6	Ctrl	Ctrl						

Tape/Reel packaging



Device	Package Type	Pin	MPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
K78_MT-1000R4	SMD	6	1200	330.0	12.4	9.56	7.56	3.5	12.0	16.0	Q1

Notes:

- 1. For additional information on Product Packaging please refer to www.mornsun-power.com. Tape/Reel packaging bag number: 58240017;
- 2. Refer to IPC 7093 for the welding process design of this product. For detailed operation guidance, please refer to Hot Air Gun Welding Operation Instruction for DFN Package Product or Welding Operation Instruction for DFN Package Product;
- 3. The maximum capacitive load offered were tested at nominal input voltage and full load;
- 4. Unless otherwise specified, parameters in this datasheet were measured under the conditions of Ta=25°C, humidity<75%RH with nominal input voltage and rated output load;
- 5. All index testing methods in this datasheet are based on our company corporate standards;
- 6. We can provide product customization service, please contact our technicians directly for specific information;
- 7. Products are related to laws and regulations: see "Features" and "EMC";
- 8. Our products shall be classified according to ISO14001 and related environmental laws and regulations, and shall be handled by qualified units.

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